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	APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/782,648		02/19/2004		Nelson Diaz	P00951-US	4118
	3017	7590	05/19/2005		EXAMINER	
			IS & HOLMES, LT	MAI, LAM T		
	101 DYER STREET 5TH FLOOR				ART UNIT	PAPER NUMBER
	PROVIDENCE, RI 02903				2819	

DATE MAILED: 05/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			An					
	Application No.	Applicant(s)	- 1/					
	10/782,648	DIAZ ET AL.						
Office Action Summary	Examiner	Art Unit						
	LAM T. MAI	2819						
The MAILING DATE of this communication a	appears on the cover sheet w	vith the correspondence addres	is					
Period for Reply	N V IC CET TO EVEIDE A	AONTHIC EDOM						
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a r - If NO period for reply is specified above, the maximum statutory perion - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a eply within the statutory minimum of thiod will apply and will expire SIX (6) MORULE, cause the application to become A	reply be timely filed rty (30) days will be considered timely. NTHS from the mailing date of this commul BANDONED (35 U.S.C. § 133).	nication.					
Status								
1) Responsive to communication(s) filed on 19	February 2004.							
2a) ☐ This action is FINAL . 2b) ☑ TI	his action is non-final.							
3) Since this application is in condition for allow	•	• •	rits is					
closed in accordance with the practice unde	r Ex parte Quayle, 1935 C.[D. 11, 453 O.G. 213.						
Disposition of Claims								
4) Claim(s) 1-20 is/are pending in the application	on.							
4a) Of the above claim(s) is/are withd	rawn from consideration.							
5)⊠ Claim(s) <u>10-20</u> is/are allowed.	Claim(s) <u>10-20</u> is/are allowed.							
6) Claim(s) <u>1 and 2</u> is/are rejected.	Claim(s) <u>1 and 2</u> is/are rejected.							
7)⊠ Claim(s) <u>3-9</u> is/are objected to.								
8) Claim(s) are subject to restriction and	l/or election requirement.							
Application Papers								
· · · · · · · · · · · · · · · · · · ·	9) The specification is objected to by the Examiner. 0) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
Replacement drawing sheet(s) including the corre	•	• •	• •					
11) The oath or declaration is objected to by the	Examiner. Note the attache	d Office Action or form PTO-1	52.					
Priority under 35 U.S.C. § 119								
 12) Acknowledgment is made of a claim for foreignal All b) Some * c) None of: 1. Certified copies of the priority docume 	nts have been received.							
2. Certified copies of the priority docume								
3. Copies of the certified copies of the priority documents have been received in this National Stage								
application from the International Bure	• • • • • • • • • • • • • • • • • • • •	المدينة محمد						
* See the attached detailed Office action for a li	scorine ceruneu copies not	TECEIVEU.						
Attachment(s)								
1) Notice of References Cited (PTO-892)		Summary (PTO-413)						
2)		s)/Mail Date nformal Patent Application (PTO-152))					
Paper No(s)/Mail Date 2000.								

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DETAILED ACTION

Specification

The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Information Disclosure Statement

The information disclosure statement (IDS) submitted on 2/19/2004 has been considered by the examiner

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Steinbach et al (USP 6,490,005).

Regarding claim 1, Steinbach discloses in figures 3A and 3B an integrated circuit that teaches a plurality of post amplifiers (508, 510, and 512) for receiving data signals and outputting amplified data signals (BV and BBV) and the post amplifiers are capable of operating with multiple different input voltage levels (AV and ABV) (see figures 3A and 3B and col. 6-8)

Claim Rejections - 35 USC § 103

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The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Steinbach et al (6,490,005).

Regarding claim 2, Steinbach discloses in figures 3S and 3B an integrated circuit that teaches multiples post amplifiers receiving data signal and outputting amplified data signal and the post amplifiers are operating with multiple different input voltage levels (see figures 3A and 3B and col. 6-8). Steinbach fails to teach or suggest the input voltage levels are in the range 2.5V and 3.3V. However, It would be obvious to one of ordinary skill in the art at the time of the invention to specify input voltage level at 2.5V and 3.3V for obtaining optimum benefit of the circuit.

Allowable Subject Matter

Claims 3-9 are objected to as being dependent upon a rejected base claim, but they would be allowable if they are rewritten in independent form including all of the limitations of the base claim and any intervening claims. The prior art fails to teach or suggest a switch located between second groups of post amplifiers and an input power supply. and an auto zero circuitry integrally connected to each individual post amplifier for performing an auto zero function of zeroing the dc component of the received data signals.

Claims 10-16 are allowable. The prior art fails to teach or suggest a switch located between second group of post amplifier and an input power supply wherein first group of post amplifiers is always electrically coupled to input power supply to receive power during normal operation and wherein the second group of post amplifiers are selectively electrical coupled to the input power supply to receive power only when the switch is turned on.

Claims 17-20 are allowable. The prior art fails to teach or suggest an auto zero circuitry for performing an auto zero function of zeroing the dc component of received data signals wherein the auto zero circuitry does not require the use of an external capacitor to perform the auto zero function.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to LAM T. MAI whose telephone number is (571)272-1807. The examiner can normally be reached on 6:00 am - 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-

free).

Lam T Mai Art Unit 2819